



PATENT APPLICATION Attorney's Do. No. 1482-129

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled GAIN AND PHASE DETECTOR HAVING DUAL LOGARITHMIC AMPLIFIERS, the specification of which:

	[] [X] []	ممامه استنام	28, 2000 as Application	(1)	ι αρριιοα	.0.0,
identified spea	cificatio	by state that I have review on, including the claims,	as amended by any affect	Idilicit	Terented	bove- to
patentability a	as defin	nowledge the duty to disc ed in Title 37, Code of F	ederal Regulations, Sec.	1.50.		
	d) of an	by claim foreign priority by foreign application(s) fired below any foreign apperfore that of the application	for patent or inventor's complication for in	ventor's	s certific	0010
Prior Foreign					ority Cla	imed
(Number)		(Country)	(Day/Month/Year Fil		[] Yes	[] No
any United S	I here States p	eby claim the benefit und rovisional application list	er Title 35, United States ted below:	s Code,	, Sec. 11	9(e) of
Provisional .	Applica	tion No.		<u>Filin</u>	ig Date	
60/231,505				Septem	<u>nber 9, 2</u>	000

I hereby claim the benefit under Title 35, United States Code, Sec. 120 of any United States application(s), or Sec. 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this

application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of Title 35, United States Code, Sec. 112. I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, Sec. 1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

(App. Serial No.)	(Filing Date)	(Status -patented, pending, etc.)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Bcé Ci

Full name of sole or first inventor: Barrie Gilbert

Inventor's signature:

Residence:

Portland, Oregon

Citizenship: United Kingdom

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Barrie Gilbert

Serial No.: 09/675,902 Group Art Unit: 2858

Filed: September 28, 2000 Examiner: Not Yet Assigned

For: GAIN AND PHASE DETECTOR HAVING DUAL LOGARITHMIC

AMPLIFIERS

BOX MISSING PARTS Assistant Commissioner for Patents Washington, D.C. 20231

POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST AND REVOCATION OF PRIOR POWERS

I, William A. Wise, Assistant Clerk of Analog Devices, Inc., having a place of business at One Technology Way, Norwood, MA, assignee of the entire right, title and interest of the above-described U.S. patent application, by the assignment submitted under separate cover for recordal (copy enclosed), represent that I am empowered to sign on behalf of assignee, as evidenced by the enclosed Officer's Certificate.

As assignee of the above identified application, all powers of attorney previously given are hereby revoked and the following attorneys and/or patent agents are hereby appointed to prosecute and transact all business in the Patent and Trademark Office connected therewith:

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Date: 22,2001

ANALOG DEVICES, INC.

William A. Wise Assistant Clerk